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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kowalski, *et al.* Docket No.: INF-125
 Serial No: 10/748,332 Art Unit: 2815
 Date Filed: December 31, 2003
 Title: Device Architecture and Process for Improved Vertical Memory Arrays

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- Amendment (9 pages)

Respectfully submitted,


 Julie Russell
 Legal Assistant

Confirmation Respectfully Requested

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Commissioner for Patents
P. O. Box 1450
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AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's Office Action mailed November 5, 2004. Please amend the above-referenced application as follows.

INF-125

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Amendment

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